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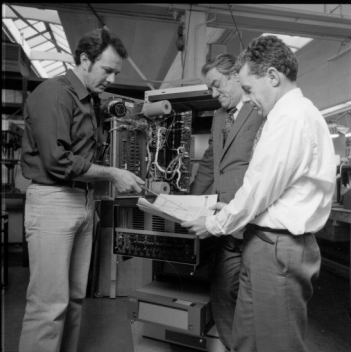


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SAFETY FILM



REDIFON COMPUTERS LIMITED

In order to fault find on the more sophisticated Printed Circuit Boards (P.C.B.) manufactured by Redifon Computers Ltd, a number of new test rigs have been built by the Unit Test Department. These rigs allow the Test Engineer easy access to all equipment necessary to perform his work efficiently. They eliminate the need for unsightly cables trailing across the department.

The photograph shows fault finding on a Scanner P.C.B. connected in one of the test rigs. All rigs are based around the E.17 mini-computer currently used in the Redifon R-range. In this instance, the Scanner under test will be used to interface between an R-range mini-computer and the Mark III microprocessor based terminals now being produced by the Company.

After the Unit Test stage is complete all P.C.B.s are run for five to ten days continuously in Systems Test prior to shipment to the customer. Any component failures during this system soak period are promptly repaired by Unit Test Engineers, so as to keep the system 'down time' to a minimum.



REDIFON COMPUTERS LIMITED

Maurice Stapleton, Electronic Wireman, working on a Seecheck terminal.



REDIFON COMPUTERS LIMITED

Printers, terminals and tape units for the R-range have been designed to accept Russian, Polish, Hungarian and Czechoslovakian alphabets. The "daughter" printed circuit board is fitted to the terminal and generates the shapes of the characters on a daughter matrix format from codes sent by the computer.





